

What Is Claimed Is:

1. An antenna amplifier (110), comprising at least one input (101) for connecting an antenna (100) and at least one output (102) for connecting a receiver (120), as well as at least one means of signal-level matching (112, 115) between input (101) and output (102), wherein a narrow-band filter (111) is situated between the input (101) and the means for signal-level matching (112, 115), the mid-frequency of the filter passband being able to be changed and tuned to the receive channel of the receiver (120).
2. The antenna amplifier (110) as recited in Claim 1, wherein the pass frequency of the filter (111) is tunable via a tuning signal, which is applied to a control terminal of the antenna amplifier (110) and is generated by the receiver (120).
3. The antenna amplifier (110) as recited in Claim 1 or 2, wherein the pass frequency of the filter (111) is tunable via a tuning signal, which is applied to the output (102) of the antenna amplifier (110) and is generated by the receiver (120).
4. The antenna amplifier (110) as recited in the preceding claim, wherein the tuning signal applied to the output (102) of the antenna amplifier (110) and evaluated in the antenna amplifier is a d.c. voltage or an analog amplitude-modulated, frequency-modulated, or pulse-width-modulated signal, or a digital data stream.
5. The antenna amplifier (110) as recited in one of the preceding claims, wherein it includes a device (116) for splitting up or

filtering out the signal components at the output (102).

6. The antenna amplifier (110) as recited in one of the preceding claims,
wherein a supply voltage for the antenna amplifier may be applied to the output (102) of the antenna amplifier (110).
7. The antenna amplifier (110) as recited in one of the preceding claims,
wherein the device (116) also includes a storage unit for storing tuning information.
8. The antenna amplifier (110) as recited in one of the preceding claims,
wherein further control signals are applied to a control terminal or to the output (102).
9. The antenna amplifier (110) as recited in one of the preceding claims,
wherein it includes a means for generating a return signal applied to a control terminal or to the output (102).
10. The antenna amplifier (110) as recited in one of the preceding claims,
wherein the reactions to control signals, such as a switchover to previously stored tuning information, occur in response to defined internal and/or external events.
11. The antenna amplifier (110) as recited in one of the preceding claims,
wherein the reactions to control signals, such as a switchover to previously stored tuning information, occur at defined times, the time information being provided by a module for generating a time reference.

12. A receiver (120) having means for generating a tuning signal and/or additional control signals for an antenna amplifier (110) as recited in one of the preceding claims.
13. The receiver (120) as recited in the preceding claim, the means for generating the tuning signal and/or additional control signal including a module (151), whose input is connected to a microcontroller (230) or to an internal tuning signal (145), and to whose output the tuning signal is applied in a form suitable for transmission to an antenna amplifier as recited in one of Claims 1 through 9.
14. The receiver as recited in one of Claims 12 or 13, having means for detecting and/or evaluating information signals, which are generated by the antenna amplifier and are transmitted in addition to the radio signals.
15. A receiving system, in particular a receiving system of a motor vehicle, having a receiver (120) and an antenna amplifier (110) as recited in one of the preceding claims.